

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Kiyoshi NAKAI et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The documents on the attached List are cited in the specification, on page 2, and their relevance is indicated therein.

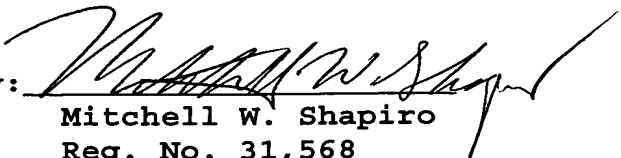
Document AK corresponds to U.S. Patent Nos. 5,602,772 and 5,629,887.

Respectfully submitted,

MWS:lmb

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